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Christian Doppler
Forschungsgesellschaft

Security and Quality Improvement in the Production System Lifecycle

Christian Doppler Forschungsgesellschaft

Securing Cyber-Physical Systems through Digital Twins

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“ A digital twin is an integrated [...] **simulation** of a [...] system that uses the best available **physical models, sensor updates, [...] etc.**, to **mirror** the life of its [...] flying twin. ”
 Shafto et al. [7]

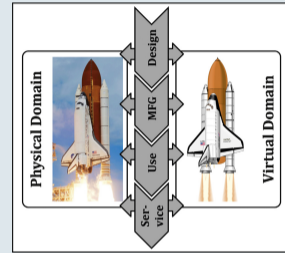


Figure: The vision according to [6].



(a) Nuclear power plant © AlMare, CC BY-SA 3.0

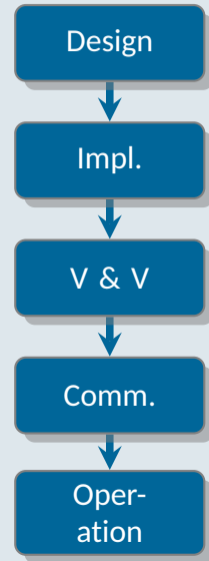
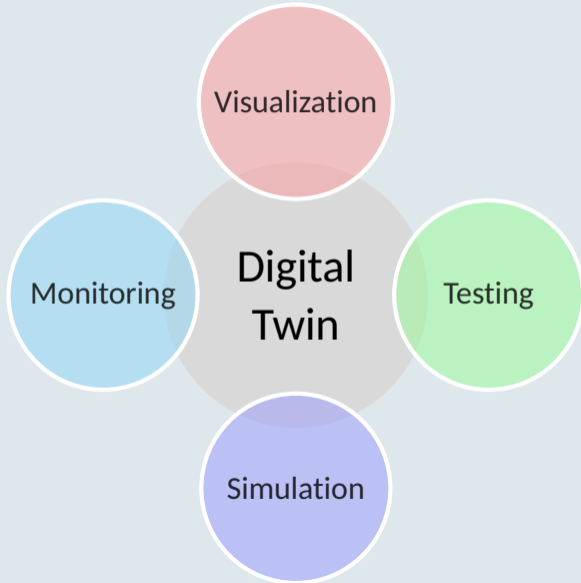


(b) Industrial Robots © Mixabest, CC BY-SA 3.0



(c) Tesla Model S © raneko, CC BY 2.0

Use Cases of the Digital Twin Concept



Intrusion Detection

- Knowledge-based
- Behavior-specification-based
- Process knowledge

Example: Sequence Attacks (e.g., [1])

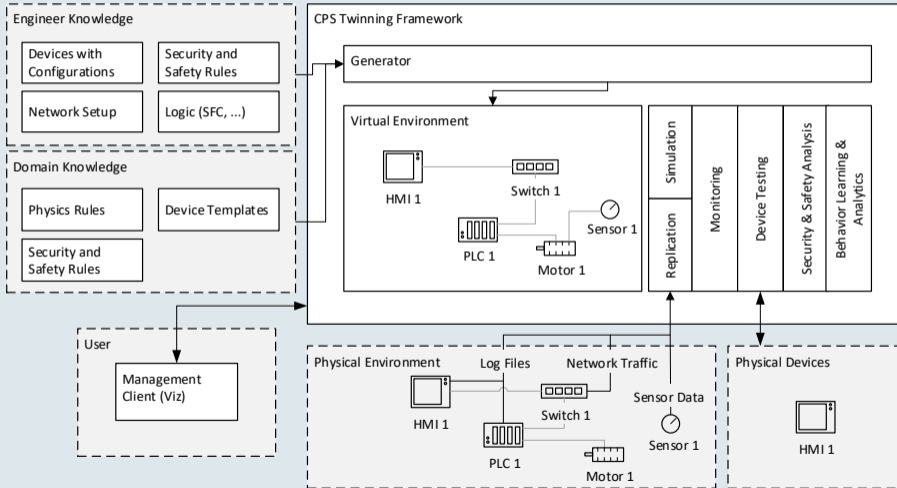


Detecting Misconfigurations

- Manipulation by attacker
- Detect unknown devices

Penetration Testing

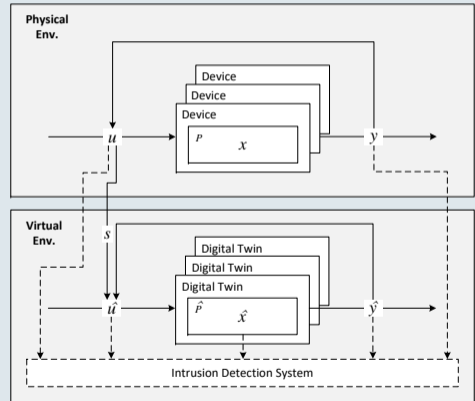
- No interference with live system
- No test environment required



State Replication

A FSM, $P := (X, x_0, U, Y, \delta, \lambda)$

- X is the finite set of states
- $x_0 \in X$ is the initial state
- U is the finite set of inputs
- Y is the finite set of outputs
- δ is the transition function
- λ is the output function

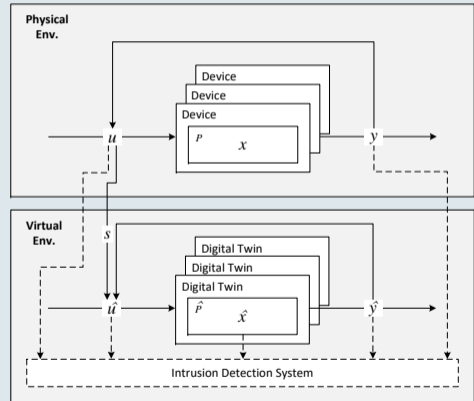


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- X is the finite set of states
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We expect that $P = \hat{P}$

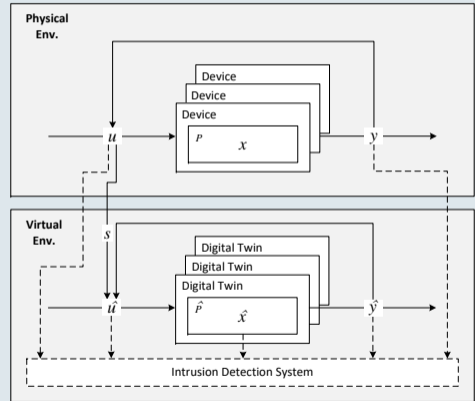
Thus, $\delta(x, u) = \hat{\delta}(\hat{x}, \hat{u}) \Leftrightarrow x' = \hat{x}'$,
provided that $(x = \hat{x}) \wedge (u = \hat{u})$.



S , denotes the set of stimuli

$$S := \{z \in \hat{U} \mid z \in U \wedge z \notin Y^*\}$$

Each digital twin should produce $\hat{y} \in \hat{Y}$ by itself.



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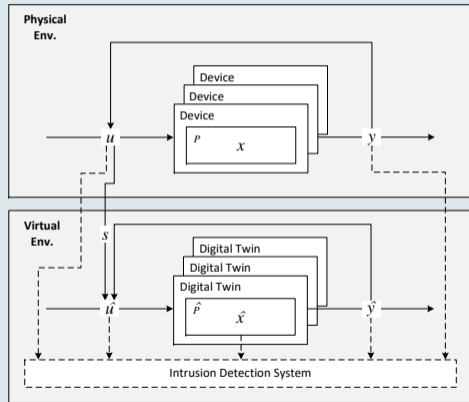
$$S := \{z \in \hat{U} \mid z \in U \wedge z \notin Y^*\}$$

Each digital twin should produce $\hat{y} \in \hat{Y}$ by itself.

Use specification of CPS to identify stimuli

Let $f: U^* \cup Y^* \rightarrow S^*$ be a partial function, then I is defined as follows:

$$I := \{j \in U^* \cup Y^* \mid f(j) \in S^*\}.$$

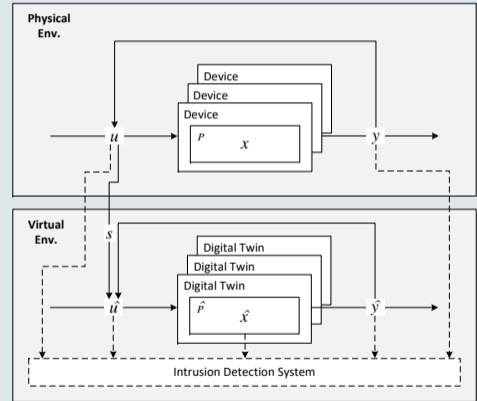


Replicate stimuli

Next, $j \in U^* \cup Y^*$ will be observed and checked whether $j \in I$.

Since $j \in I \Leftrightarrow f(j) \downarrow, s \in S^*$, the value of f of j , is fed to the respective digital twin.

Hence, $\hat{\delta}(\hat{x}, s) = \hat{x}'$.



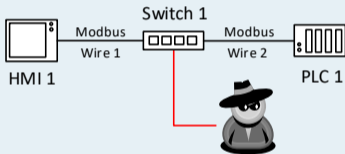
- Conveyor belt
- HMI & PLC digital twins exist
- Communication via Modbus TCP/IP
- Definition of *f*
- AutomationML [2]

```
1 <InternalElement Name="LogicalNetwork" ID="c51...">
2   <InternalElement Name="ModbusRequests" ID="ce1...">
3     <InternalElement
4       ↪ Name="StartConveyorBeltModbusReadRequest"
5       ↪ ID="0e5...">
6       <Attribute Name="functionCode"
7         ↪ AttributeDataType="xs:integer">
8         <Value>3</Value>
9       </Attribute>
10      ...
11     <InternalLink Name="HMI1 StartConveyorBelt -
        ↪ PLC1 Modbus 400001" RefPartner-
        ↪ SideA="{068...}:StartConveyorBelt"
        ↪ RefPartnerSideB="{29b...}:1" />
12     <RoleRequirements RefBaseRoleClass-
13       ↪ Path="/ModbusReadHoldingRegisters"
14       ↪ />
15   </InternalElement>
16   ...
```

Intrusion Detection

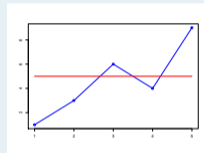
Implicit

Network Layout

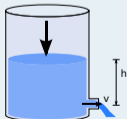


Explicit

Thresholds for Variables



Laws of Physics



Torricelli's Law © LimoWreck, CC BY-SA 3.0

Relationship Between States



Behavior-specification-based IDS

Assumptions

- Specification of CPS defines the correct behavior
- Digital twin follows state of its physical counterpart

Inner workings

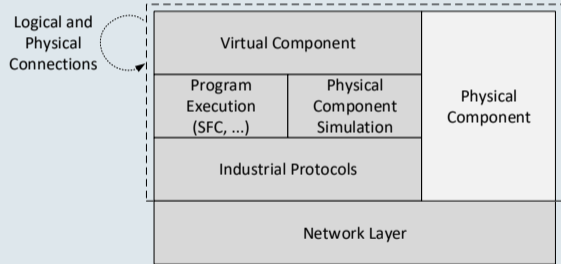
- Comparison between $p \in U^* \cup Y^*$ and $v \in \hat{U}^* \cup \hat{Y}^*$
- Predefined features (e.g., Modbus FC)

Benefits & drawbacks

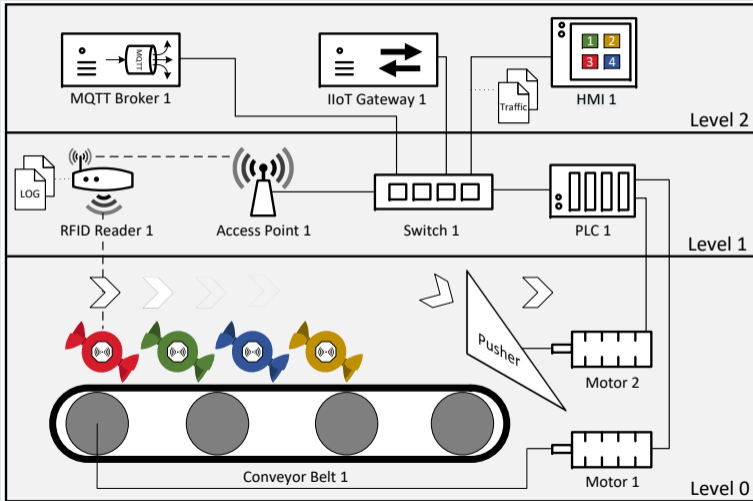
- Automatic in-depth checks without causing any risks of interference
- Risk of replicating malicious stimuli

Proof of Concept

- Based on Mininet [5]
- Integration of MatIEC transcompiler
- GitHub Repos:
 - CPS Twinning
 - CPS State Replication



```
1 mininet> twinning /home/user/ConveyorSystem.aml
2 mininet> nodes
3 available nodes are:
4 HMI1 PLC1 Switch1 c0
5 mininet> links
6 Switch1-eth1<->HMI1-eth0 (OK OK)
7 Switch1-eth2<->PLC1-eth0 (OK OK)
8 mininet> show_tags PLC1
9 Name          |Class      |Type
10 -----
11 ENABLE        |var        |bool
12 PTO            |var        |bool
13 Q10            |out        |bool
14 Q00            |out        |bool
15 START         |mem        |bool
16 STOP          |mem        |bool
17 VELOCITY      |mem        |int
18 ...
19 mininet> get_tag PLC1 START
20 False
21 mininet> set_tag PLC1 START True
22 mininet> get_tag PLC1 START
23 True
```



```
1 <ExternalInterface Name="Velocity"  
  ↳ RefBase="/VariableInterface">  
2 <Attribute Name="refURI">  
3 <Value>file:///SFC.xml#Velocity</Value>  
4 <Constraint Name="Safety Rule Motor">  
5 <OrdinalScaledType>  
6 <RequiredMaxValue>  
7 60  
8 </RequiredMaxValue>  
9 </OrdinalScaledType>  
10 ...
```

```
1 <InternalElement Name="VelocityConstraint"  
  ↳ ID="e0b...">  
2 <Attribute Name="operator"  
  ↳ AttributeDataType="xs:string">  
3 <Value>equals</Value>  
4 </Attribute>  
5 <InternalLink Name="VelocityConstraint"  
  ↳ PLC1 - HMI1"  
  ↳ RefPartnerSideA="{133...}:Velocity"  
  ↳ RefPartnerSideB="{068...}:Velocity"  
  ↳ />  
6 ...  
7 </InternalElement>
```

```
1 INFO:root:'Velocity' value changed 0 -> 20 in device 'HMI1'.  
2 INFO:root:'VELOCITY' value changed 0 -> 100 in device 'PLC1'.  
3 WARNING:root:ALERT! 'PLC1' tag [Velocity=100] exceeds max value of 60.  
4 WARNING:root:ALERT! 'HMI1' tag [Velocity=20] does not equal 'PLC1' tag [Velocity=100].
```

IDS Output

```
1 14:04:55.178 - Count [pCandy=1,vCandy=1].
2 +-----+
3 | candy|
4 +-----+
5 |Cherry|
6 +-----+
7 14:06:06.392 - Count [pMQTT=8,vMQTT=1].
8 +-----+-----+-----+-----+-----+-----+-----+-----+-----+
9 | eth.src| eth.dst| ip.src| ip.dst|...|...|...|...|mqtt.len|mqtt.topic|mqtt.msg|
10 +-----+-----+-----+-----+-----+-----+-----+-----+-----+
11 |08:00:...|f8:1e:...|192.168.0.61|192.168.0.32| 3| 0| 0| 0| 11| candy| Mint|
12 ...
13 |08:00:...|f8:1e:...|192.168.0.61|192.168.0.32| 3| 0| 0| 0| 11| candy| Mint|
14 +-----+-----+-----+-----+-----+-----+-----+-----+-----+
```

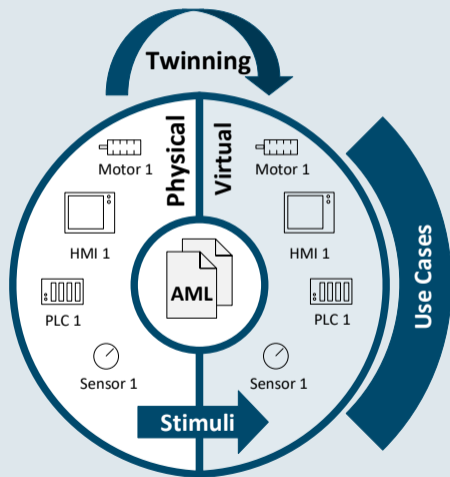
```
1 15:07:21.065 - Count [pCandy=1,vCandy=1].
2 +-----+
3 | candy|
4 +-----+
5 | Mint|
6 +-----+
```

Contribution

- Generation of digital twins from specification
- State replication

Challenges

- Specification often non-existent or incomplete
- Performance issues
- High overhead, even though automatic generation is feasible



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Sequence-aware intrusion detection in industrial control systems.
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